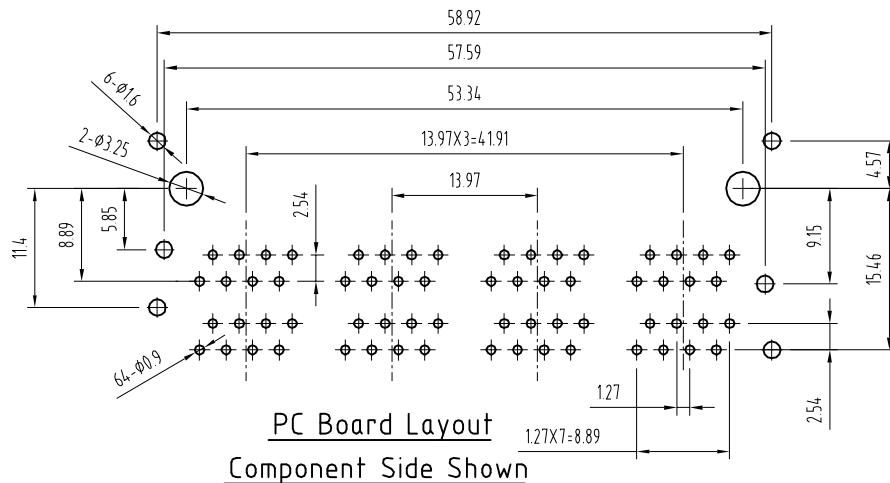
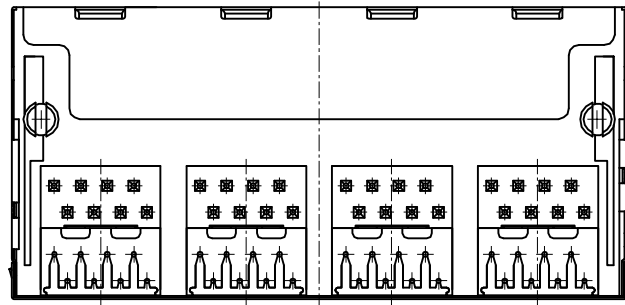
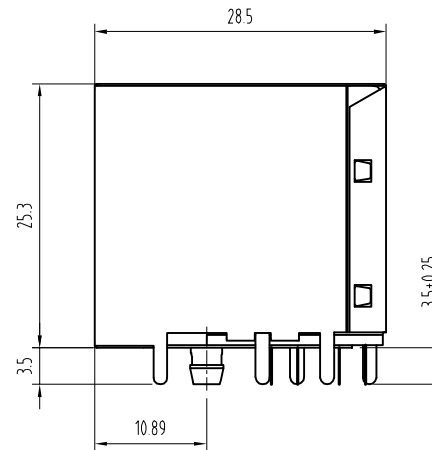
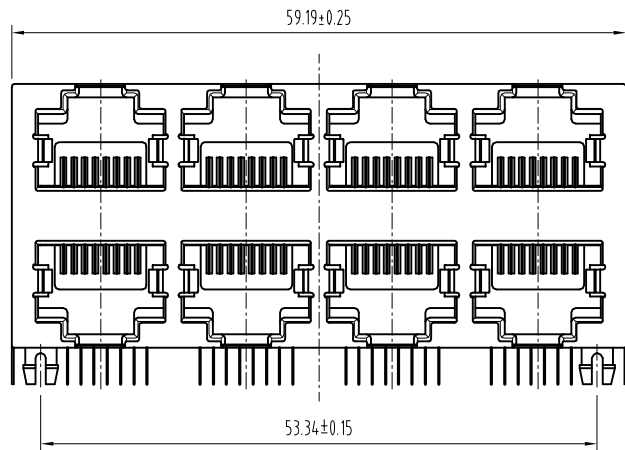




PC Board Layout
Component Side Shown

REVISION RECORD				
REV	ECO	DESCRIPTION	DRFT	CHKD

NOTES:

MATERIAL:

- 1.HOUSING MATERIAL :GLASS FILLED POLYESTER UL94V-0.
- 2.CONTACT MATERIAL :PHOSPHOR BRONZE $\phi 0.46\text{mm}$.
- 3.PLATING :GOLD PLATING OVER NICKEL.
- 4.SHIELD :0.23mm THICKNESS COPPER ALLOY WITH NICKEL PLATED.

ELECTRICAL:

- 1.VOLTAGE RATING :125 VAC RMS.
- 2.CURRENT RATING :15 AMP.
- 3.CONTACT RESISTANCE :30 MILLIOHMS MAX.
- 4.INSULATION RESISTANCE :500 MEGOHMS MIN @ 500V DC .
- 5.DIELECTRIC WITHSTANDING RESISTANCE :1000V AC RMS 50Hz. 1MIN.

MECHANICAL:

- 1.DURRABILITY :750 CYCLES MIN.
- 2.PCB RETENTION PRE-SOLDER :1 LB MIN.

ENVIRONMENTAL:

- 1.STORAGE : -40°C~+85°C
2. OPERATION : 0°C TO 70°C.

DEFINIED LETS	MM INCHI		DFTO: SHB	DATE: 2007.05.03	Coorle Technology			
	TOLERANCES EXCEPT AS NOTED		CHKD: LANBO	DATE: 2007.05.03				
			MFG:	DATE:				
	MM .1 ± 0.15 .01 ± 0.175 .001 ± 0.05		APPL:	DATE:	TITLE 5921 8P8CS 2*4			
			MATERIAL :					
	ANGLES ± 0.5		QT'Y :					
	 THIRD ANGLE PROJECTION		FINISH :		DRAWING NO. TES981006		SIZE	REV
			SCALE :		/PART NO.		A3	0
					DO NOT SCALE DRAWING		SHEET X	OF Y